

METHOD AND APPARATUS FOR LOGICALLY  
EXPANDING THE WIDTH OF MEMORY

ABSTRACT OF THE DISCLOSURE

A method and apparatus for minimizing memory required for storing non-binary  
5 width data structures is disclosed. The non-binary width data structure is segmented  
into plural segments . The segments are stored in a plurality of memory blocks.  
Mapper logic maps a logical address to a physical address in the memory blocks to  
access non-binary width entries in the non-binary width data structure stored in the  
memory blocks.

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